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FORTRESS FUN-DING

INTRODUCTION

A modern Coin Telephone set must provide new features to meet the consumer telephone industry demands as coin telephone receivers become obsolete in many communities. Among these are a high-quality, versatile, electronic coin value signalling system and a variable initial rate coin toll-collecting circuit. A new electronic tone generating circuit has replaced the mechanical gong system of signalling coin values to pay stations. Also, a new, more pleasing, more versatile audible tone signals which have been adopted by most Bell Operating companies to signal coin denominations from a coin telephone to a toll operator. In addition, it provides coin value pulses which are compatible with the Centrex Data Station (CDS) terminal.

The coin signalling circuitry, developed for new single-slot coin telephones, eliminates the bulky mechanical gong, making room for other electronic circuitry. It also helps to improve the quality of coin signalling and toll collection. The new circuitry is a transistorized version which works directly with the normal variations in line length between the telephone set and the switching office. Signalling reliability is improved by making the telephone handset receiver during the time the tones are being sent.

The totalizing circuit illustrates the flexibility gained by using electronics in coin sets. It will allow the operating company to select the desired number of coins to be collected for any given call. To measure by a more exact method, each coin in the set is used with a switching device that provides a fine accuracy totalizing service. The output of the totalizing circuit may be used to control an electronic switch which is interfaced with the toll switching office. With such a system, the coin set is operative with either a dial office or a manual switchboard. Any service call for the coin telephone installed that rate is deposited.

SELECTION OF TECHNOLOGY

The coin signalling portion has been approached in the past with a combination of electromechanical and bipolar circuitry. Some of the problems associated with reliability, power supply, and the need to switch the handset receiver during the signalling period have led to the tone circuit. Thus, important design objectives for the new circuitry were to operate on the power available from the telephone line and to be directly connected in parallel with the voice circuit of the telephone set. Complementary MOS technology was chosen for the coin signalling circuit because of its low power consumption, high noise immunity, and wide supply voltage range. Complementary MOS is ideally suited to the coin telephone set application since it requires little power and has a high noise characteristic. The major part of the system, all the digital circuitry and the timing circuitry, and patterns for the tone oscillator, are fabricated on two complementary MOS integrated circuit chip packages. Discrete components are used for circuit and power handling situations such as relay driving, and transient and polarity protection.

CIRCUIT DESCRIPTION

When the telephone handset is lifted, a line switch contact applies central office battery to the circuit via the Ring and Tip leads (Fig. 1). A power supply regulator circuit provides a constant voltage supply voltage by approximately 1.2V dc to maintain the minimum 3V dc requirement of the circuit, corrects line voltage polarity, and protects the circuit against transients on the line. An external reset circuit provides a power-on reset pulse to the coin signalling and the toll collection relay. The tone circuit gives an immediate disabling signal to the coin signalling circuitry which disables the circuitry during operations such as dial pulsing and coin collect/refund. Coin inputs appear as contact closures on three different input leads, one for each of the 5, 10 and 25¢ denomination coins. The coin signalling circuitry includes two coded tone bursts for transmission to the central office (Fig. 2). Digital signals are also provided at the start and finish of each cycle to operate the coin collect/refund relay. The oscillator frequency determining resistor 71 (Fig. 3) is normally biased off by a positive voltage impedance to the line. At coin mode the negative voltage from the coin signalling and totalizer circuit receives from the coin signalling circuit a pulse for every 5¢ increment of deposited voltage. It initiates the amount and when the correct initial bias is reached, actuates a relay. This relay is used to control the coin collect/refund relay and the emergency calling capability, to activate an electronic switch. The initial rate may be varied from 5¢ to 40¢ in 5¢ increments.

To improve the line balance ground isolation is provided. This circuit senses the presence of loop current and opens the ground path during the talking period.

CMOS TONE OSCILLATOR

The getable CMOS sine wave oscillator used in the coin signalling circuit utilizes three CMOS inverters (Fig. 4a, 4b) in Fig. 3) in the following way. Inverter #1 is connected as a Colpitts type oscillator with the frequency determining elements being inductor L1 and capacitors C1 and C2. Inverters #2 and #3 provide additional current gain and regulation and are applied to the gate of #2 by providing an ac ground at the node of C1 and C2. The ac ground is provided by connecting the input and output of #3 together. By choosing the ratio of the feedback resistance R1 and the operating point of #1 (R1 = 5 kilohm divider R1, R2 and thermistor R3) stabilities can be obtained over wide limits with regard to temperature and supply voltage variations. Power for #2 and #3 is provided via series resistor R4 which are switched by logic level (Fig. 2). The sine wave is present throughout the signalling period. Inverter #4 is used as a buffer between the oscillator and the coin signalling circuitry for shaping the coin pulses presented by the logic. This method of shaping a constant sine wave source to achieve a fast rise time for the output tone burst. To have accurate tone bursts on and off requires that the oscillator be capable of off-chip components to achieve an acceptably fast turn-on time. The oscillator circuit uses only four chip components are gain and frequency controlling elements.

Fig. 1 Tone Signaling and Coin Circuit Block Diagram

Fig. 2 Timing Diagram of Tone Signaling Sequence

Fig. 3 CMOS Tone Oscillator

Fig. 4 Signalling Level Reserved at Six-Signaling Office

Fig. 5 Coin Signalling Circuit Block Diagram

Fig. 6 Coin Signalling Circuit Schematic

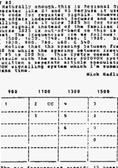

Fig. 7 Coin Signalling Circuit Schematic

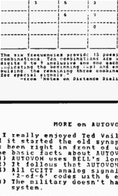

Fig. 8 Coin Signalling Circuit Schematic

Fig. 9 Coin Signalling Circuit Schematic

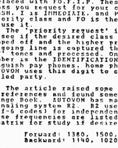

Fig. 10 Coin Signalling Circuit Schematic

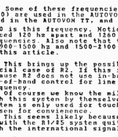

Fig. 11 Coin Signalling Circuit Schematic

Fig. 12 Coin Signalling Circuit Schematic

Fig. 13 Coin Signalling Circuit Schematic

LETTERS FROM READERS

On Long Island if you dial 958 you get a computer with a voice which announces the number from which you are calling. I have been told that if there is a tap on the line the voice doesn't say anything so it's one way of checking a line for a tap--- I don't know if this is a fact and, if so, it is because of some condition on the line which prevents the computer from uttering anything or if the phone co programs the computer not to respond to 958 calls which come from numbers that it knows on--or what happens if it isn't a MA Bell approved tap. I also don't know if 958 produces any response from fones outside of my own area--but in Long Island it does produce a response. If it isn't a MA Bell approved tap. I also don't know if 958 produces any response from fones outside of my own area--but in Long Island it does produce a response. I've been to people's homes where they have an unlisted phone and have thoughtfully removed the number from the phone so their casual guests can't get it-- ol' 958 has come through every time!

Free magazines? Easy! Next time you go to a doctor or dentist's office, sort through the mags in his waiting room. See any you like? Pull off the address sticker on the front cover, attach it to the change of address coupon in the magazine, and send it back to the publisher. In a few weeks you'll be getting the magazine (hopefully at your P.O. box). Sometimes it takes the better part of the year before Doc realizes what happened and then gets it straightened out! Good luck!

Super Grinch

Producing Short-Run Printed Circuit Boards

By Jack of the subsystem in the Bell Laboratories Model Shop, Holmdel, New Jersey, we have the need of making designs for the rapid prototyping of printed circuit boards. Typically, we are developing circuits which are complex enough to require printed circuit boards, but which will be changed frequently during the development phase. Thus, we need a process which will produce boards of high quality and reliability, but which will allow us to make changes in the circuit rapidly and inexpensively. Our boards typically contain from 50 to 200 holes and have a single layer of printed circuitry. Most of the circuits are digital, and the boards contain a large number of integrated circuit packages. Many of the boards contain wire-wrap pins, and we frequently use wire-wrap techniques to make changes in the circuitry.

Materials

We have found that Photo Resist 2 (PR-2, Type 2 Kodak Metal Resist developer and Kodak Metal Etchant) works well for our purposes. We have also used the Kodak Photo Resist (KPR) and Kodak Thin Film Resist (KTFR) with good results. The PR-2 is a positive resist which is developed with Kodak Metal Resist developer and removed with Kodak Resist Stripper. The KPR and KTFR are negative resists which are developed with Kodak Micro Resist developer and removed with Kodak Resist Stripper. The KPR is a relatively slow resist which requires a long exposure time, but it has the advantage of being relatively insensitive to ultraviolet light. The KTFR is a fast resist which requires a short exposure time, but it must be handled in subdued light. Both the KPR and KTFR are supplied with the Kodak wet developing kit. The PR-2 is a positive resist which is developed with Kodak Metal Resist developer and removed with Kodak Resist Stripper. The PR-2 is a relatively slow resist which requires a long exposure time, but it has the advantage of being relatively insensitive to ultraviolet light. The PR-2 is